

266/ Her 11-0402

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)	ACCURAGE AND ADDADAGE CO.
Avneesh Agrawal, et al.) For:))	METHOD AND APPARATUS FOR CHIP-RATE PROCESSING IN A CDMA SYSTEM
Serial No. 09/852,436)	
Filed: May 9, 2001)	

INFORMATION DISCLOSURE STATEMENT Technology Center 2600 UNDER 37 CFR § 1.97(c)

Commissioner of Patents Washington, D.C. 20231

Dear Sir:

Applicants, through their attorney, submit herewith references in accordance with 37 CFR § 1.56.

The citation of these references is not intended to constitute an admission that any reference provided is prior art to the invention of this application unless specifically designated as such.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on:

October 24, 2002

(Date of Deposit)

Parla D. Kasmedo

Name of the Person Making Deposit)

(IDS_SEARCH.VER1.1-9/13/01)

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The filing of this document shall not be construed to mean that any search has been made or, that if made such search was complete or exhaustive, or that no other material information as defined in 37 CFR § 1.56 exists. A list of the references is set forth on Form PTO-1449, which is enclosed herewith in duplicate along with a copy of each cited reference. Applicants respectfully request that the Examiner return to Applicants the enclosed copy of the Form PTO-1449 indicating consideration of the references.

The undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. A copy of the search report dated August 20, 2002 is enclosed.

Respectfully submitted,

Dated:

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